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CONFIRMATION NO. 7345

STEVEN M. RABIN **SUITE 1111** 1725 K ST., N.W. WASHINGTON, DC 20006

FILING RECEIPT

OC000000012043278

Date Mailed: 03/08/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kazutaka Shibata, Kyoto, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/284,318 10/31/2002

Foreign Applications

JAPAN 2001-336827 11/01/2001 JAPAN 2001-338110 11/02/2001

If Required, Foreign Filing License Granted: 03/03/2004

Projected Publication Date: 06/17/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and method for manufacturing same

Preliminary Class

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